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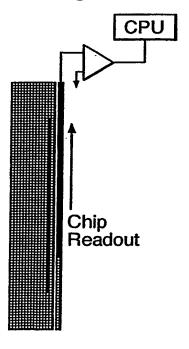
Fig.5a.

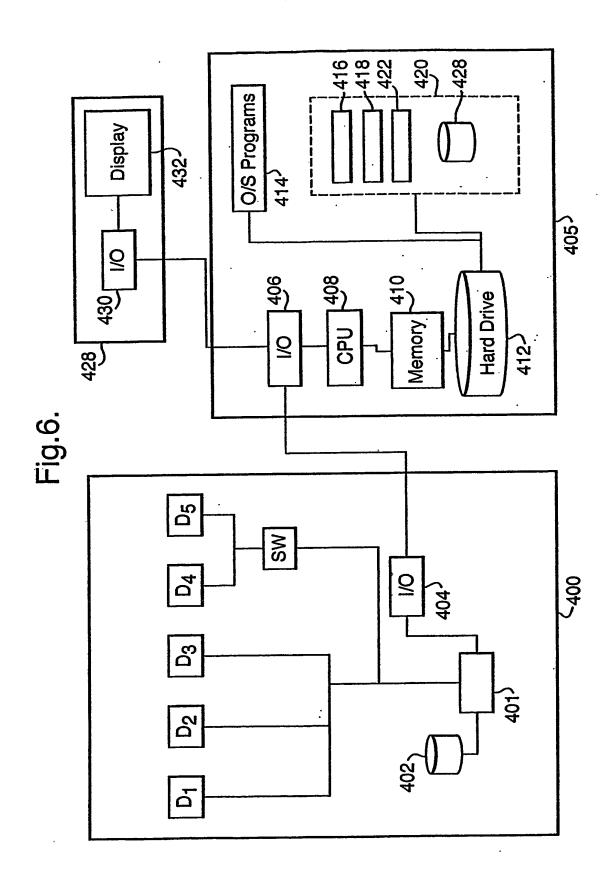
CPU

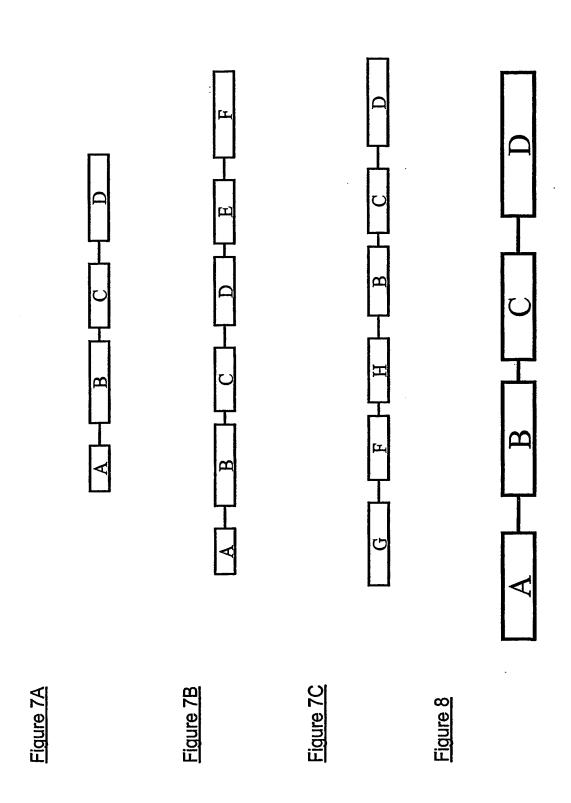
Emission
Stripe

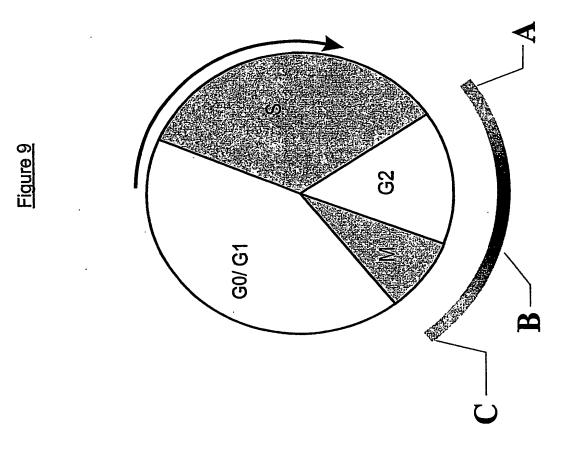
CCD
Chip
Readout
Register

Fig.5c.









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Figure 10

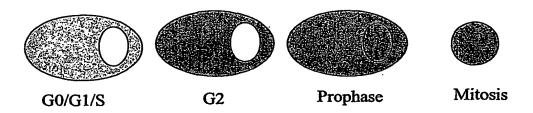
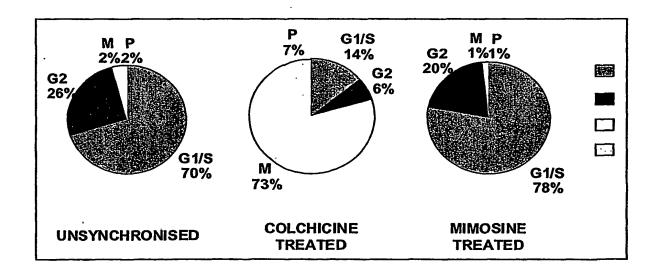


Figure 11



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Figure 12

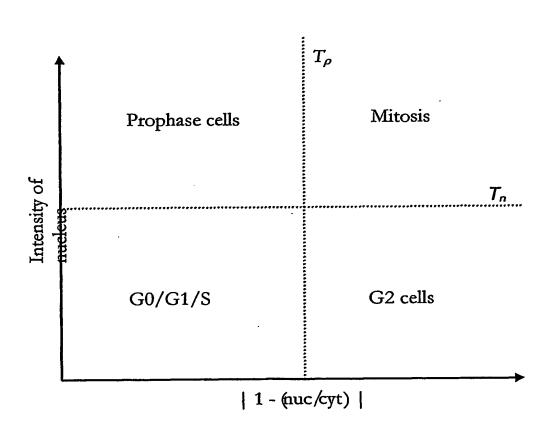


Figure 13

